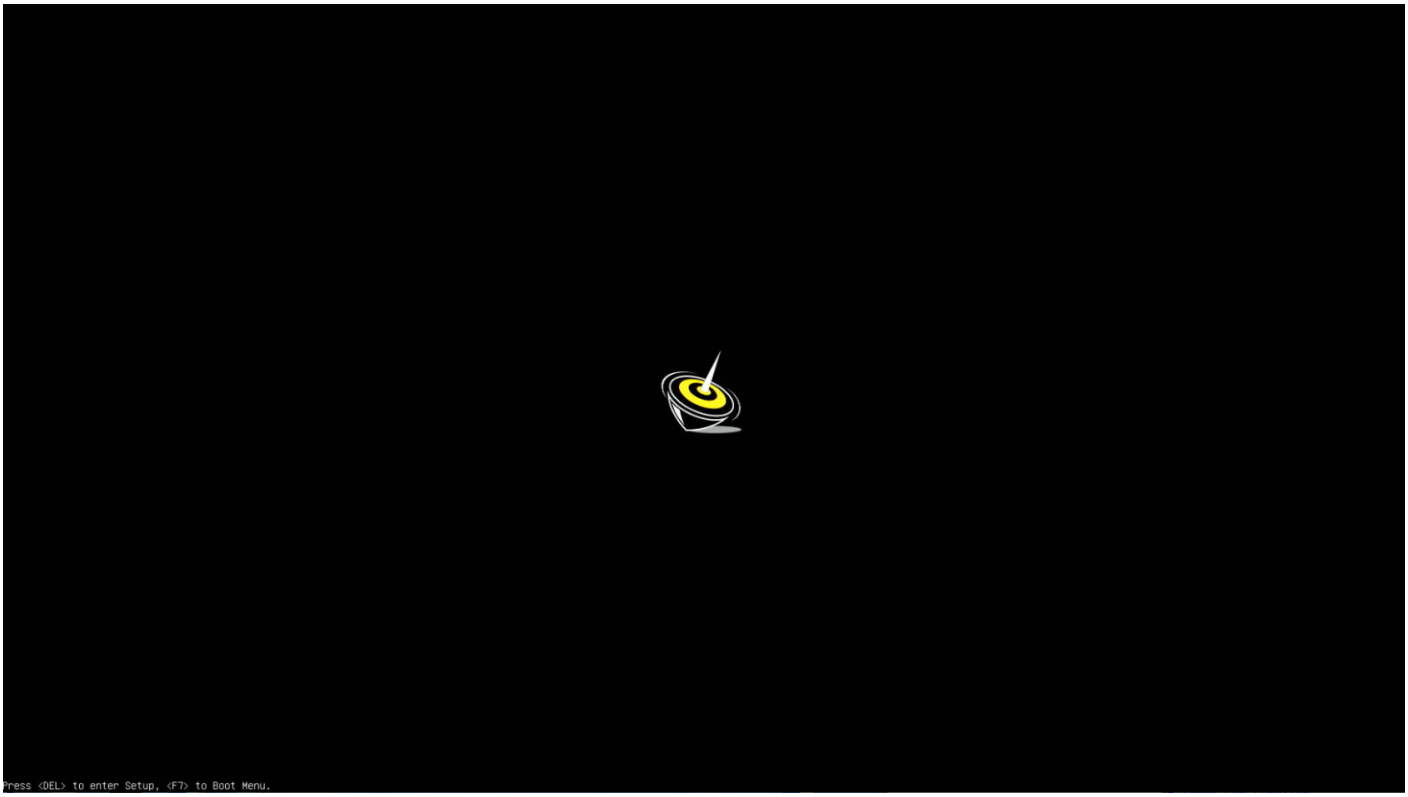
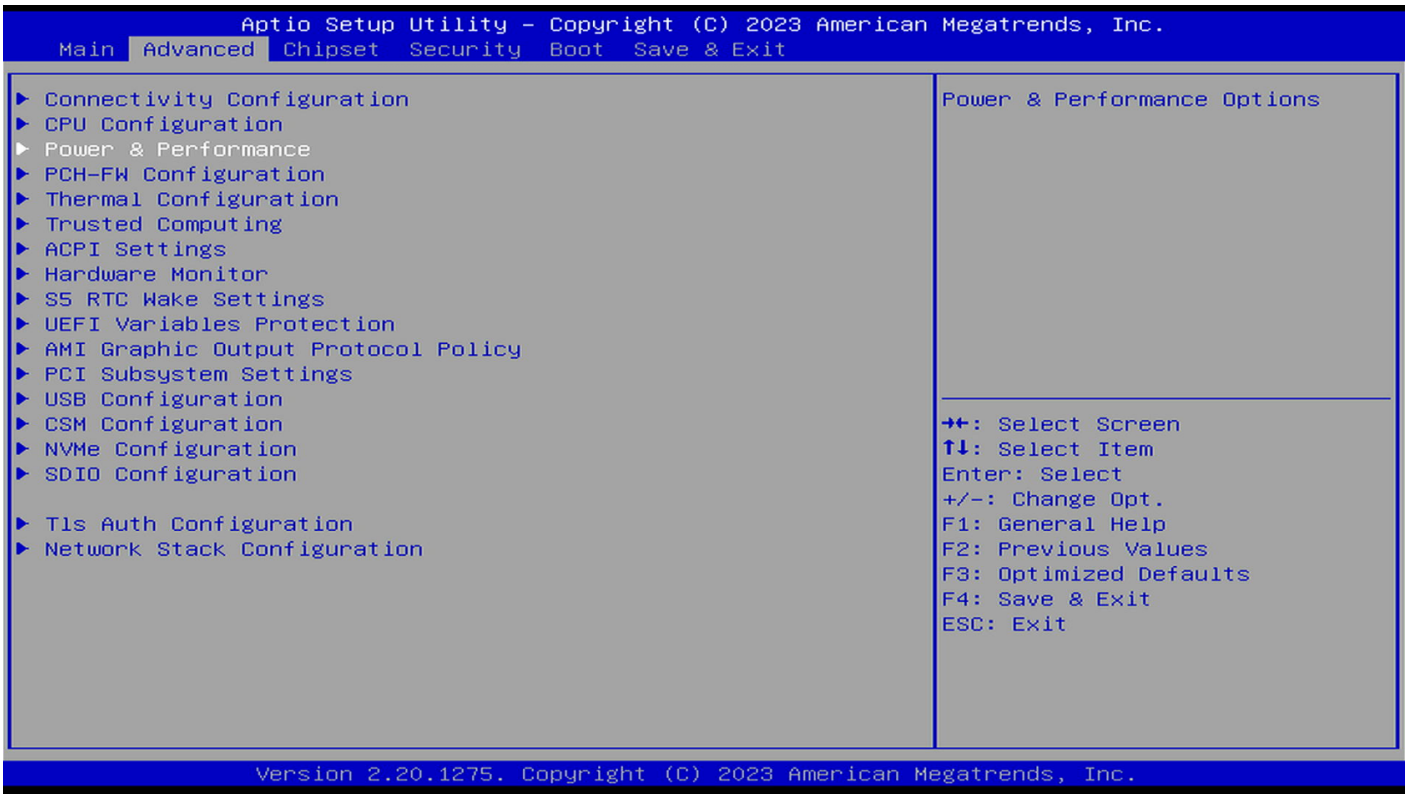
??CPU??

???????

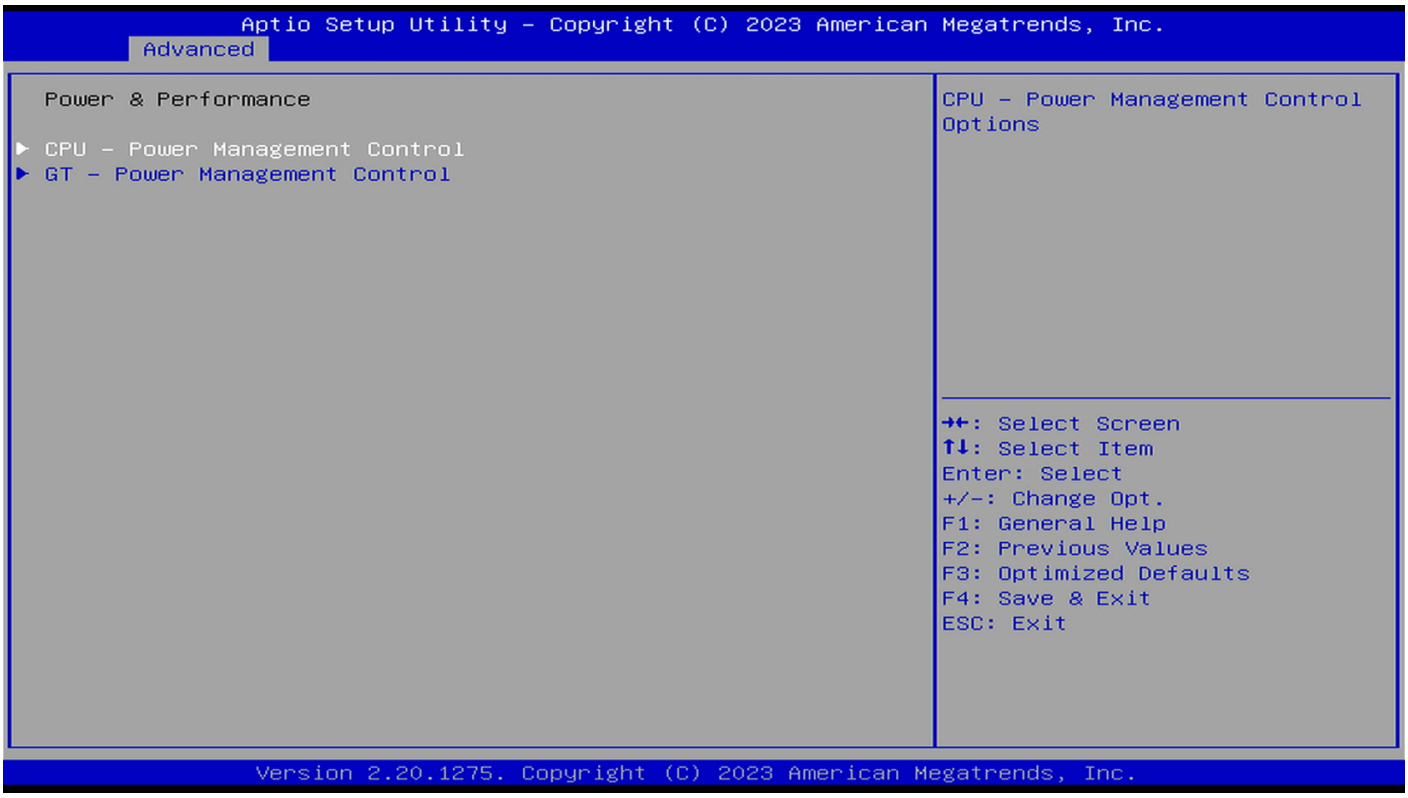
1.??????????????????logo????????Del??????BIOS



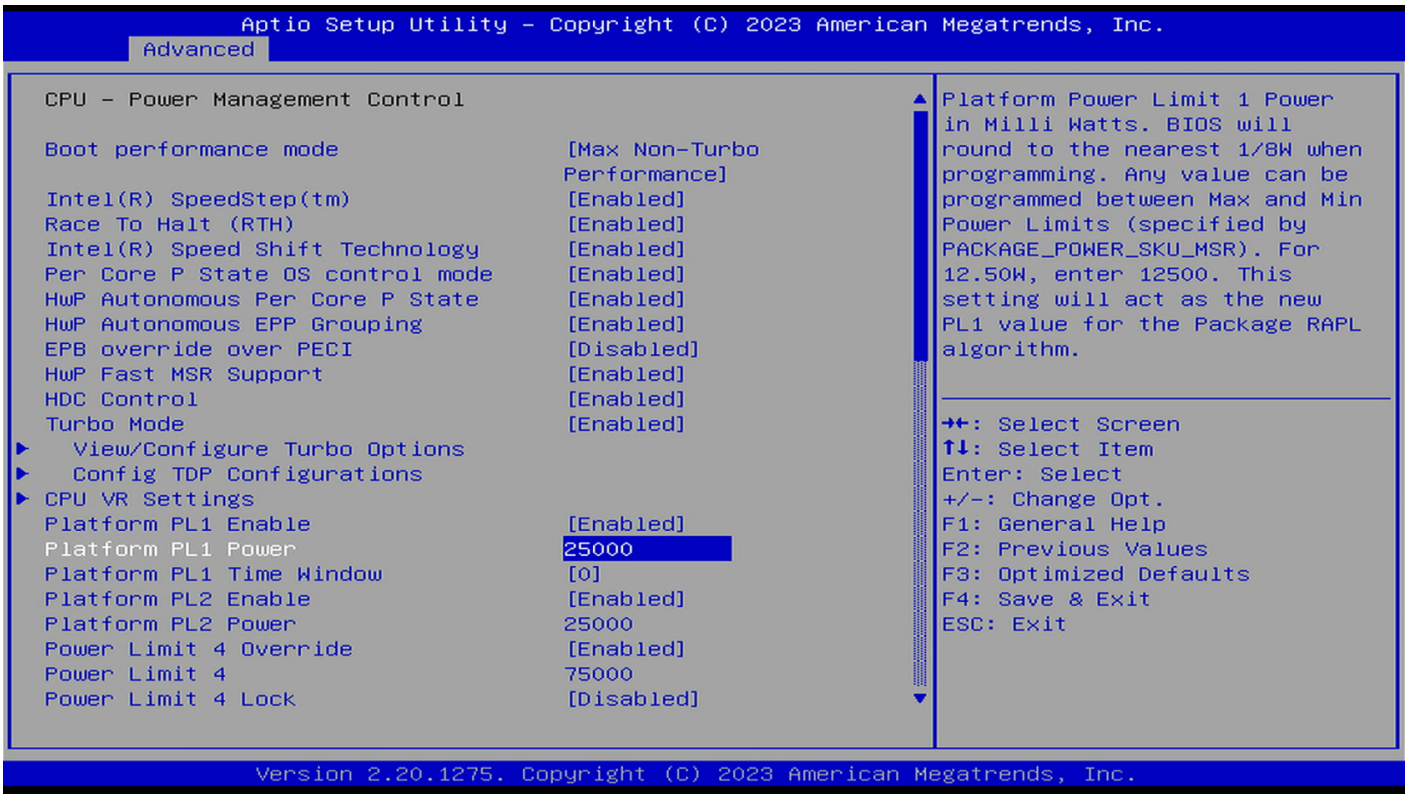
2.????Advanced???????Power & Performance?



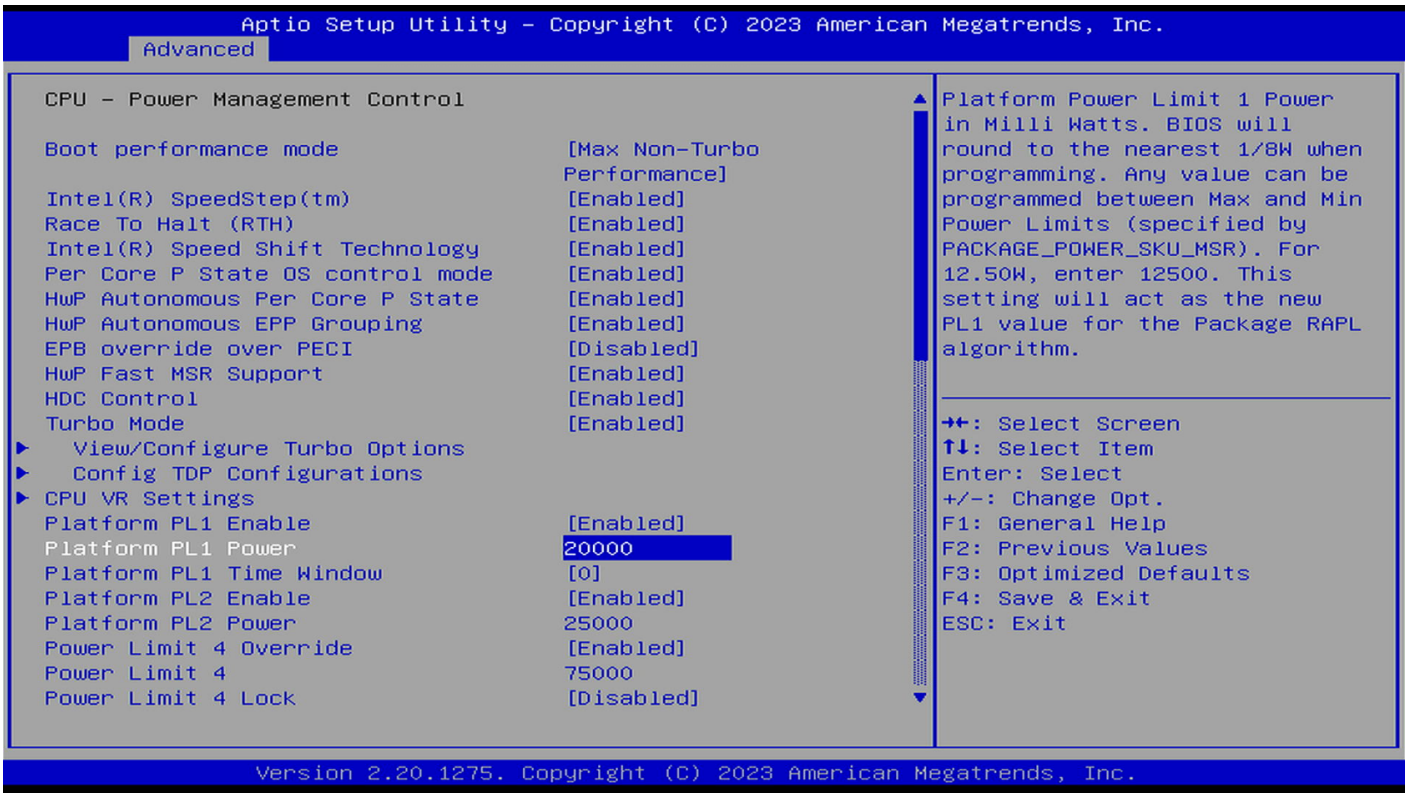
4.???CPU-Power Management Control???



5.???Platform Pl Power? ???????25000????25W???


Aptio Setup Utility – Copyright (C) 2023 American Megatrends, Inc.
Advanced
CPU – Power Management Control
Boot performance mode [Max Non-Turbo Performance]
Intel(R) SpeedStep(tm) [Enabled]
Race To Halt (RTH) [Enabled]
Intel(R) Speed Shift Technology [Enabled]
Per Core P State OS control mode [Enabled]
HwP Autonomous Per Core P State [Enabled]
HwP Autonomous EPP Grouping [Enabled]
EPB override over PECI [Disabled]
HwP Fast MSR Support [Enabled]
HDC Control [Enabled]
Turbo Mode [Enabled]
View/Configure Turbo Options
Config TDP Configurations
CPU VR Settings
Platform PL1 Enable [Enabled]
Platform PL1 Power 25000
Platform PL1 Time Window [0]
Platform PL2 Enable [Enabled]
Platform PL2 Power 25000
Power Limit 4 Override [Enabled]
Power Limit 4 75000
Power Limit 4 Lock [Disabled]
Platform Power Limit 1 Power in Milli Watts. BIOS will round to the nearest 1/8W when programming. Any value can be programmed between Max and Min Power Limits (specified by PACKAGE_POWER_SKU_MSR). For 12.50W, enter 12500. This setting will act as the new PL1 value for the Package RAPL algorithm.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
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6.??????????????????????????????


Aptio Setup Utility – Copyright (C) 2023 American Megatrends, Inc.
Advanced
CPU – Power Management Control
Boot performance mode [Max Non-Turbo Performance]
Intel(R) SpeedStep(tm) [Enabled]
Race To Halt (RTH) [Enabled]
Intel(R) Speed Shift Technology [Enabled]
Per Core P State OS control mode [Enabled]
HwP Autonomous Per Core P State [Enabled]
HwP Autonomous EPP Grouping [Enabled]
EPB override over PECI [Disabled]
HwP Fast MSR Support [Enabled]
HDC Control [Enabled]
Turbo Mode [Enabled]
View/Configure Turbo Options
Config TDP Configurations
CPU VR Settings
Platform PL1 Enable [Enabled]
Platform PL1 Power 20000
Platform PL1 Time Window [0]
Platform PL2 Enable [Enabled]
Platform PL2 Power 25000
Power Limit 4 Override [Enabled]
Power Limit 4 75000
Power Limit 4 Lock [Disabled]
Platform Power Limit 1 Power in Milli Watts. BIOS will round to the nearest 1/8W when programming. Any value can be programmed between Max and Min Power Limits (specified by PACKAGE_POWER_SKU_MSR). For 12.50W, enter 12500. This setting will act as the new PL1 value for the Package RAPL algorithm.
→←: Select Screen
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Enter: Select
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F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
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7.?????????F4?????Yes??????????



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